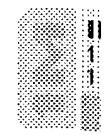# WEST Search History

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☐	L11	L10 not intel	8
☐	L10	L9 and L1	26
☐	L9	L6 same L5	197
☐	L8	L7 and L1	156
☐	L7	L6 and L5	1098
☐	L6	(cpu or $5processor or proces$4 or accelerator or engine or device or core) near3 graphics	48109
☐	L5	(cpu or $5processor) near3 core	10645
☐	L4	L2 and (cpu or $5processor or proces$4 or accelerator or engine or device or core)near3 graphics	10
☐	L3	L2 and (cpu or $5processor or proces$4)	55
☐	L2	L1 same graphic$3 near2 (image or data)	55
☐	L1	(unif$5 or shar$3) near3 cache	5532

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Results Key:
JNL = Journal or Magazine   **CNF** = Conference   **STD** = Standard

1 **Pixel processing in a memory controller**
Donovan, W.; Sabella, P.; Kabir, I.; Hsieh, M.M.;
Computer Graphics and Applications, IEEE , Volume: 15 , Issue: 1 , Jan. 1995
Pages:51 - 61

[Abstract]   [PDF Full-Text (860 KB)]   **IEEE JNL**

2 **Efficient transmission of triangle meshes to graphics processors**
Chou, P.H.; Meng, T.H.;
Signal Processing Systems, 2000. SiPS 2000. 2000 IEEE Workshop on , 11-13
2000
Pages:275 - 284

[Abstract]   [PDF Full-Text (468 KB)]   **IEEE CNF**

3 **A real-time video-image mapping using polygon rendering techniqu**
Ikedo, T.;
Multimedia Computing and Systems '97. Proceedings., IEEE International
Conference on , 3-6 June 1997
Pages:127 - 134

[Abstract]   [PDF Full-Text (724 KB)]   **IEEE CNF**

4 **A low power and high performance core for planar object overlayinc**
Budawy, W.; Bayoumi, M.;
Electronics, Circuits and Systems, 1999. Proceedings of ICECS '99. The 6th IE
International Conference on , Volume: 2 , 5-8 Sept. 1999
Pages:621 - 624 vol.2

[Abstract]   [PDF Full-Text (348 KB)]   **IEEE CNF**

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Set      Items    Description
S1       74219    CPU OR CENTRAL()PROCESS?()UNIT??
S2      121685    GRAPHIC?
S3        2020    S2()PROCESSOR? OR GPU OR GRAPHIC?()PROCESS?()UNIT??
S4      279990    TEXT OR WORDS
S5      555432    IMAG? OR PICTURE? OR PHOTO OR PHOTOGRAPH?
S6       33182    (S4 OR NONGRAPHIC? OR NON-GRAPHIC?)(10N)(S2 OR S5)
S7      114465    (ONE OR UNIFIED OR UNITED OR SHAR??? OR SINGLE)(3N)(BUFFER-
                  ?? OR CACHE OR MEMORY OR STORAGE OR STORING OR STORED)
S8           0    AU=DESORMEAUX D? OR DESORMEAUX, D?
S9       42704    HP OR HEWLETT()PACKARD
S10        741    AU=(TSAI, H? OR MAIYURAN, S? OR WANG, C? OR TSAI H? OR MAI-
                  YURAN S? OR WANG C?)
S11     143903    IC=(G06F? OR G06T?)
S12         20    S9(10N)S7
S13          0    S12(10N)S4(7N)S5
S14          8    S12 AND S11
S15          5    S14 NOT PY=>1999
S16         64    S10 AND S7
S17          0    S16(10N)S4(7N)S5
S18          0    S16(S)S4(S)S5
S19          0    S16(S)S6
S20         16    S16 AND S11
S21          4    S20 NOT PY=>1998
S22         32    S1(10N)S3(10N)S7
S23          0    S22(10N)S4(10N)S5
S24         22    S22 AND S11
S25          3    S24 NOT PY=>1998
S26          3    S25 NOT (S14 OR S20)
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File 813:PR Newswire 1987-1999/Apr 30
File 587:Jane's Defense&Aerospace 2004/Oct W5

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S1	272320	CPU OR CENTRAL()PROCESS?()UNIT??
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S3	34104	S2()PROCESSOR? OR GPU OR GRAPHIC?()PROCESS?()UNIT??
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S5	10039740	IMAG? OR PICTURE? OR PHOTO OR PHOTOGRAPH?
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S9	1084408	HP OR HEWLETT()PACKARD
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S11	4688	S9(S)S7
S12	1	S11(S)S1(S)S3
S13	4	S10 AND S7
S14	4	S13 NOT S12
S15	1	S14 NOT PY=>1998
S16	32	S1(S)S3(S)S7
S17	31	S16 NOT (S13 OR S12)
S18	12	S17 NOT PY=>1998
S19	11	RD S18 (unique items)
S20	307	S4(5N)S5(10N)S7
S21	184	S20 NOT PY=>1998
S22	1	S21(S)S1
S23	1	S22 NOT (S13 OR S12 OR S14 OR S18)
S24	29	S21(S)S2
S25	29	S24 NOT PY=>1998
S26	18	RD S25 (unique items)